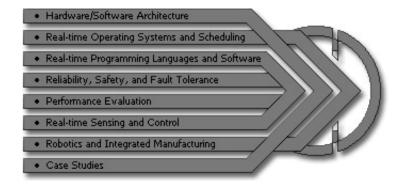
RTSS 2011 - *Program Overview*The 32<sup>nd</sup> IEEE Real-Time Systems Symposium
November 29-December 2, Vienna, Austria

November 29 <sup>th</sup>			November 30 <sup>th</sup>	December 1	1 <sup>st</sup> December 2 <sup>nd</sup>	
	_			Registration Prechtlsaal Lobby		
CRTS Prechtlsaal 1	<b>AVICPS</b> <i>Prechtlsaal</i> 2	WCTT Seminarraum Kuppel	SOMRES Seminarraum 225	Award Speech Kuppelsaal  Panel Kuppelsaal	Keynote Addı Kuppelsaaı	_
	_		•	Break	Break	Break saal Foyer Kuppelsaal
CRTS Prechtlsaal 1	AVICPS Prechtlsaal 2	WCTT Seminarraum Kuppel	SOMRES Seminarraum 225	Session 1: Mixed-Criticality Systems Kuppelsaal 2 hours	Session 3: Multiprocess Scheduling Kuppelsaa. 2 hours	: Session 7: sor Scheduling & Response Time
				Lunch Prechtisaal	Lunch Prechtlsaal TC-RTS EC mtg  Lunch Prechtlsaal	
CRTS Prechtlsaal 1	AVICPS Prechtlsaal 2	<b>WCTT</b> Seminarraum Kuppel	SOMRES Seminarraum 225	Session 2: Platforms & Implementation Issues	Session 4 WSN Algorith System Issue Improvemen	: nms, es & Session 8: CPS nts
RTSS@Work Presentations  Kuppelsaal			Ruppeisaai <b>2 hours</b>	Ruppeisaai <b>2 hours</b>	Applications	
					Kuppelsaal <b>2 hours</b>	
			Break Foyer Kuppelsaal	Break Foyer Kuppels	Break saal Foyer Kuppelsaal	
RTSS@Work Demonstrations Refreshments Kuppelsaal			Work in Progress (WiP) Session Kuppelsaal	Session 5: Design & Verification Kuppelsaa. 1.5 hours	Design & Verification II Kuppelsaal	
			WiP Poster Session Refreshments Kuppelsaal	Open TC-RT meeting Kuppelsaal	Closing Toast	
7:00pm Welcome Reception Announcement of Best Workshop Papers Prechtlsaal			8:00pm Reception Vienna City Hall Leave TU Wien at 7:30pm	7:30pm Banquet Hotel Stefan Leave TU Wie 7:10pm	nie	
Workshops			g Activities		Keynote Address	
·			Main Track			Wireless Sensor Networks
Panel			Work in Progress			Design & Verification
TC-RTS meetings				-		
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## **RTSS 2011**

The 32<sup>nd</sup> IEEE

Real-Time Systems Symposium

November 29 – December 2

Vienna, Austria

Tuesday, November 29: Workshops in Prechtlsaal 1 & 2, Seminarraum Kuppel, and Seminarraum 225; Welcome Reception in Prechtlsaal				
9:00 – 15:30 <i>Prechtlsaal 1</i>	CRTS: The Fourth Workshop on Compositional Theory and Technology for Real-Time Embedded Systems.			
9:00 – 15:30 <i>Prechtlsaal 2</i>	AVICPS: The Second Analytic Virtual Integration of Cyber-Physical Systems Workshop.			
9:00 – 15:30 Seminarraum Kuppel	WCTT: 1st International Workshop on Worst-case Traversal Time			
9:00 – 15:30 <i>Seminarraum 225</i>	SOMRES: Workshop on Synthesis and Optimization Methods for Real-time Embedded Systems			
15:45 – 16:30 <i>Kuppelsaal</i>	RTSS@Work Presentations			
16:30 – 18:00 <i>Kuppelsaal</i>	RTSS@Work: An Open Demos Session of Real-Time Techniques and Technologies.			
19:00 – 21:00 Prechtlsaal	Welcome Reception: Wine Tasting and Announcement of Best Workshop Papers			
Wednesday, November 30: Meetings	in Kuppelsaal; Reception at Vienna City Hall			
8:55 – 9:30	Welcome and Award Speech Introduction: Scott Brandt			
Welcome and Award Speech Kuppelsaal	Award Speech: <b>Ted Baker</b>			
	Moderator: Rolf Ernst			
9:30 - 10:30  Industry/Government Panel	FUELLING THE INNOVATION PIPELINE			
Kuppelsaal	With the participation of <b>Ted Baker</b> (NSF), <b>Jorge Pereira</b> (EC), <b>Stefan Poledna</b> (TTTech) and <b>Bert Boeddeker</b> (DENSO Automotive Deutschland)			
10:30 - 11:00	Break in Foyer Kuppelsaal			
	CERTIFICATION-COGNIZANT TIME-TRIGGERED SCHEDULING OF MIXED- CRITICALITY SYSTEMS Sanjoy Baruah and Gerhard Fohler			
11:00 - 13:00	EFFECTIVE AND EFFICIENT SCHEDULING OF CERTIFIABLE MIXED-CRITICALITY SPORADIC TASK SYSTEMS			
<u>Session 1: Mixed-Criticality Systems</u> <i>Kuppelsaal</i>	Nan Guan, Pontus Ekberg, Martin Stigge and Wang Yi			
<i>пирреізааі</i>	DESIGN OPTIMIZATION OF MIXED-CRITICALITY REAL-TIME APPLICATIONS ON COST-CONSTRAINED PARTITIONED ARCHITECTURES  Domitian Tamas-Selicean and Paul Pop			
Chair: Sathish Gopalakrishnan	RESPONSE-TIME ANALYSIS FOR MIXED CRITICALITY SYSTEMS Sanjoy Baruah, Alan Burns and Rob Davis			
13:00-14:00	Lunch in <i>Prechtlsaal</i>			
	ANYTIME ALGORITHMS FOR GPU ARCHITECTURES Rahul Mangharam and Aminreza Abrahimi Saba			
14:00-16:00	SLEEPY SLOTH: THREADS AS INTERRUPTS AS THREADS Wanja Hofer, Daniel Lohmann and Wolfgang Schroder-Preikschat			
Session 2: Platforms & Implementation Issues Kuppelsaal	STACK MANAGEMENT FOR HARD REAL-TIME COMPUTATION IN A COMPONENT- BASED OS			
(continues on next page)	Qi Wang, Jiguo Song and Gabriel Parmer			

Chair: Stefan Petters (continuation of Session 2)	RGEM: A RESPONSIVE GPGPU EXECUTION MODEL FOR RUNTIME ENGINES Shinpei Kato, Karthik Lakshmanan, Aman Kumar, Mihir Kelkar, Yutaka Ishikawa and Ragnunathan Rajkumar
16:00-16:30	Break in Foyer Kuppelsaal
16:30-18:00 <i>Kuppelsaal</i>	Work in Progress Session
18:00-18:30 <i>Kuppelsaal</i>	Work in Progress Poster Session – <i>Refreshments served</i>
20:00 – 21:30	Reception at Vienna City Hall – Leave TU Wien at 7:30pm
Thursday, December 1: Meetings in Kupp	pesaal; Banquet at Hotel Stefanie
9:00-10:30	Keynote Introduction: Luis Almeida
<u>Keynote Address</u> Kuppelsaal	Keynote Speaker: <b>Bert Boeddeker<sup>1</sup>, Rafael Zalman<sup>2</sup></b> <sup>1</sup> DENSO Automotive Deutschland GmbH, Germany <sup>2</sup> Infineon Technologies AG, Germany
	AUTOSAR AT THE CUTTING EDGE OF AUTOMOTIVE TECHNOLOGY
10:30 - 11:00	Break in <i>Foyer Kuppelsaal</i>
11:00 - 13:00	SOFT REAL-TIME ON MULTIPROCESSORS: ARE ANALYSIS-BASED SCHEDULERS REALLY WORTH IT? Christopher J. Kenna, Jonathan L. Herman, Björn B. Brandenburg, Alex F. Mills and James H. Anderson
Session 3: Multiprocessor Scheduling Kuppelsaal	RUN: OPTIMAL MULTIPROCESSOR REAL-TIME SCHEDULING VIA REDUCTION TO UNIPROCESSOR Paul Regnier, George Lima, Ernesto Massa, Greg Levin and Scott Brandt
Chair: Bjorn Andersson	THE PARTITIONED EDF SCHEDULING OF SPORADIC TASK SYSTEMS Sanjoy Baruah
	WORKLOAD-AWARE PARTITIONING FOR MAINTAINING TEMPORAL CONSISTENCY UPON MULTIPROCESSOR PLATFORMS Jianjun Li, Jian-Jia Chen, Ming Xiong and Guohui Li
13:00-14:00	Lunch in <i>Prechtlsaal</i> (Meeting of the TC-RTS Executive Committee)
	ON ENERGY-EFFICIENT TRAP COVERAGE IN WIRELESS SENSOR NETWORKS Junkun Li, Jiming Chen, Shibo He, Tian He, Yu Gu and Youxian Sun
14:00 - 16:00	WIZSYNC: EXPLOITING WI-FI INFRASTRUCTURE FOR CLOCK SYNCHRONIZATION IN WIRELESS SENSOR NETWORKS Tian Hao, Ruogu Zhou, Guoliang Xing and Matt Mutka
Session 4: WSN Algorithms, System Issues and Improvements	
Kuppelsaal	IMPROVING LINK QUALITY BY EXPLOITING CHANNEL DIVERSITY IN WIRELESS SENSOR NETWORKS Manjunath Doddavenkatappa, Mun Choon Chan and Ben Leong
Chair: Chenyang Lu	WICOP: ENGINEERING WIFI TEMPORAL WHITE-SPACES FOR SAFE OPERATIONS OF WIRELESS BODY NETWORKS IN MEDICAL APPLICATIONS  Yufei Wang, Qixin Wang, Zheng Zeng, Guanbo Zheng and Rong Zheng
16:00-16:30	Break in Foyer Kuppelsaal
16:30 - 18:00	MEETING END-TO-END DEADLINES THROUGH DISTRIBUTED LOCAL DEADLINE ASSIGNMENTS Shengyan Hong, Thidapat Chantem and Xiaobo Sharon Hu
Session 5: Design & Verification I Kuppelsaal	SCALABLE AND PRECISE REFINEMENT OF CACHE TIMING ANALYSIS VIA MODEL CHECKING Sudipta Chattopadhyay and Abhik Roychoudhury
Chair: Jian-Jia Chen	SCALABLE FIXED-POINT FREE INSTRUCTION CACHE ANALYSIS Damien Hardy, Benjamin Lesage and Isabelle Puaut
18:00 – 18:30 ( <i>Kuppelsaal</i> )	Open meeting of the TC-RTS – IEEE Technical Committee on Real-Time Systems
19:30 – 22:00	Banquet at Hotel Stefanie – Leave TU Wien at 7:10pm

### AUTO- 10:30  ### AU	Friday, December 2: Meetings in Kuppelsaal; Closing Toast in Kuppelsaal Lobby				
Chair: Enrico Bini   Session 5: Multi-Core Systems   ARITHATION IN HARD REAL-TIME MULTICORE SYSTEMS   Mar-KI Yoon, Jung-Eun Kim and Lui Sha   Chair: Wolfgang Schroder-Preikschat   RESPONSE TIME ANALYSIS OF HIERARCHICAL SCHEDULING: THE SYNCHRONIZED DEFERRABALE SERVERS APPROACH   Haltao Zhu, Steve Goddard and Matthew Dwyer					
ARBITRATION IN HARD REAL-TIME MULTICORE SYSTEMS   Man-KI Yoon, Jung-Eun Kim and Lui Sha	9:00 - 10:30	OPTIMIZING TUNARUE WOFT WITH SHARED RESOURCE ALLOCATION AND			
DEFERRABLE SERVERS APPROACH Haitao Zhu, Steve Goddard and Matthew Dwyer  10:30 – 11:00  Broak in Foyer Kuppelsaal  IMPROVING FEASIBILITY OF FIXED PRIORITY TASKS USING NON-PREEMPTIVE REGIONS Marko Bertogna, Giorgio Buttazzo and Gang Yao  CACHE-RELATED RE-EMPTION DELAY AWARE RESPONSE TIME ANALYSIS FOR FIXED PRIORITY RE-EMPTIVE SYSTEMS  Session 7: Scheduling and Response Time Analysis  Kuppelsaal  SESUINCE AUGMENTATION BOUNDS FOR APPROXIMATED DEMAND BOUND FIXED PRIORITY SYSTEMS Sebastian Altmeyer, Robert Davis and Claire Maiza  RESOURCE AUGMENTATION BOUNDS FOR APPROXIMATED DEMAND BOUND FINITIONS Jan-Jia Chen and Samarjit Chakraborty  SCHEDULING PRIODIC REAL-TIME TASKS WITH HETEROGENEOUS REWARD REQUIREMENTS Hong Hou and P.R. Kumar  Lunch in Prechtsaal  ON THE FEASIBILITY OF LINEAR DISCRETE-TIME SYSTEMS OF THE GREEN SCHEDULING PROBLEM Zheng Li, Per Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behl, George Pappas and Rahul Mangharam  14:00 - 16:00  TIME-PREDICTABLE COMPUTER ARCHITECTURE FOR CYBER-PHYSICAL SYSTEMS: DIGITAL EMULATION OF POWER ELECTROWICS SYSTEMS MICH & Kinsy, Dusan Majstorovic, Nikola Celanovic, and Srinivas Devadas  Kuppelsaal  PHOTONET: A SIMILARITY AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS Mid Yusuf Uddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  TELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRUACY PROTECTION Shaojle Tanj, Haitao Zhang, Kang Yang Li and Jiankang Han  16:00-16:30  Break in Foyer Kuppelsaal  TIMING ANALYSIS OF A PROTECTED OPERATING SYSTEM KERNEL Bernard Blackham, Yao Shi, Sudipta Chattopadhyay, Abhik Roychoudhury and Gernot Heiser  FEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR HARD REAL-TIME SYSTEMS Mohammad Khavari Tavana, Mohammad Salehi and Alireza Ejiali  Chair: Tullio Vardanega  PCM-FIL: A WRITE-ACTIVITY-AWARE PARA RAND FLASH MEMORY MANAGEMENT SCHEME FOR PCM-BASED ENERGED EN SYSTEM DIO LIU, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao		ARBITRATION IN HARD REAL-TIME MULTICORE SYSTEMS			
11:00 - 13:00  IMPROVING FEASIBILITY OF FIXED PRIORITY TASKS USING NON-PREEMPTIVE REGIONS Marko Bertogna, Giorgio Buttazzo and Gang Yao  CACHE-RELATED PRE-EMPTION DELAY AWARE RESPONSE TIME ANALYSIS FOR FIXED PRIORITY PRE-EMPTIVE SYSTEMS Sebastian Altmeyer, Robert Davis and Claire Maiza  RESOURCE AUGMENTATION BOUNDS FOR APPROXIMATED DEMAND BOUND FUNCTIONS Jain-Jia Chen and Samarjit Chakraborty  Chair: Enrico Bini  SCHEDULING PERIODIC REAL-TIME TASKS WITH HETEROGENEOUS REWARD REQUIREMENTS: Hong Hou and P.R. Kumar  Lunch in Prechtsaal  ON THE FEASIBILITY OF LINEAR DISCRETE-TIME SYSTEMS OF THE GREEN SCHEDULING PROBLEM SCHEDULING PROBLEM SCHEDULING PROBLEM Chair Serior Systems  MY PROBLEM SYSTEMS OF THE GREEN SCHEDULING PROBLEM SYSTEMS OF THE GREEN SCHEDULING PROBLEM SCHEDULING PROBLEM SYSTEMS MICHER Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  PHOTONET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS MY YUSU Iddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  IELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  IFLOSCAM: JOENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  IFLOSCAM: JOENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  IFLOSCAM: JOENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION SHAOTING SYSTEM KERNEL BERNARD BEASCH SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  IFLOSCAM: JOENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  IFLOSCAM: JOENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION SHAOTING SWITH PRIVACY PROTECTION SHAOTING SWITH PRIVA	Chair: Wolfgang Schroder-Preikschat	DEFERRABLE SERVERS APPROACH			
### Arch Bertogna, Giorgio Buttazzo and Gang Yao  ### CACHE-RELATED PRE-EMPTION DELAY AWARE RESPONSE TIME ANALYSIS FOR PURED PRIORITY PRE-EMPTIVE SISTEMS  ### Sebastian Altmeyer, Robert Davis and Claire Maiza  ### RESOURCE AUGMENTATION BOUNDS FOR APPROXIMATED DEMAND BOUND FUNCTIONS  ### Jian-Jia Chen and Samarjit Chakraborty  ### CHEDULING PERIODIC REAL-TIME TASKS WITH HETEROGENEOUS REWARD REQUIREMENTS  ### I-hong Hou and P.R. Kumar  ### Lianch in Prechtissal  ### ONT HET FEASIBILITY OF LINEAR DISCRETE-TIME SYSTEMS OF THE GREEN SCHEDULING PROBLEM  ### Zheng Li, Pei-Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behl, George Pappas and Rahul Mangharam  ### JIANCH PREDICTABLE COMPUTER ARCHITECTURE FOR CYBER-PHYSICAL SYSTEMS: DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  ### PHOTONET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS Med Yusuf Uddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  ### HONGEN TO PROVER THE PROTECTION Shadje Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  ### IEOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shadje Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  ### IFONE SESSION 9: Design & Verification II  ### RUDON Shadje Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  ### PEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR HARD REAL-TIME SYSTEMS Mohammad Salehi and Alireza Ejlali  ### PEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR PARASED ENBERGY MANAGEMENT AND ALISH MEMORY MANAGEMENT SCHEME FOR POM-BASED ENBEDDED SYSTEM DUG Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao	10:30 - 11:00	Break in <i>Foyer Kuppelsaal</i>			
Session 7: Scheduling and Response Time   Analysis   Sebastian Altmeyer, Robert Davis and Claire Maiza   RESOURCE AUGMENTATION BOUNDS FOR APPROXIMATED DEMAND BOUND   FUNCTIONS   Jian-Jia Chen and Samarjit Chakraborty   SCHEDULING PERIODIC REAL-TIME TASKS WITH HETEROGENEOUS REWARD   REQUIREMENTS   T-Hong Hou and P.R. Kumar	11:00 - 13:00	REGIONS  Marko Bertogna, Giorgio Buttazzo and Gang Yao			
Ruppelsaal FUNCTIONS Jian-Jia Chen and Samarjit Chakraborty  Chair: Enrico Bini  SCHEDULING PERIODIC REAL-TIME TASKS WITH HETEROGENEOUS REWARD REQUIREMENTS I-Hong Hou and P.R. Kumar  Lunch in Prechtsaal  ON THE FEASIBILITY OF LINEAR DISCRETE-TIME SYSTEMS OF THE GREEN SCHEDULING PROBLEM Zheng Li, Pei-Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behl, George Pappas and Rahul Mangharam  14:00 - 16:00  TIME-PREDICTABLE COMPUTER ARCHITECTURE FOR CYBER-PHYSICAL SYSTEMS: DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  PHOTOMET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS MI Vasur' Uddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  TELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  16:00-16:30  Break in Foyer Kuppelsaal  TIMING AWALYSIS OF A PROTECTED OPERATING SYSTEM KERNEL Bernard Blackham, Yao Shi, Sudipta Chattopadhyay, Abhik Roychoudhury and Gernot Heiser  FEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR HARD REAL-TIME SYSTEMS Mohammad Khavari Tavana, Mohammad Salehi and Alireza Ejlali  PCM-FTL: A WRITE-ACTIVITY-AWARE NAND FLASH MEMORY MANAGEMENT SCHEME FOR PCM-BASED ENEEGDED SYSTEM Duo Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao	Session 7: Scheduling and Response Time	FIXED PRIORITY PRE-EMPTIVE SYSTEMS			
13:00-14:00  Lunch in Prechibsal  ON THE FEASIBILITY OF LINEAR DISCRETE-TIME SYSTEMS OF THE GREEN SCHEDULING PROBLEM Then I, Pei-Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behi, George Pappas and Rahul Mangharam  14:00 - 16:00  TIME-PREDICTABLE COMPUTER ARCHITECTURE FOR CYBER-PHYSICAL SYSTEMS: DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  **Ruppelsaal**  PHOTONET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS Md Yusuf Uddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  TELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  16:00-16:30  Break in Foyer Kuppelsaal  TIMING ANALYSIS OF A PROTECTED OPERATING SYSTEM KERNEL Bernard Blackham, Yao Shi, Sudipta Chattopadhyay, Abhik Roychoudhury and Gernot Heiser  **FEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR HARD REAL-TIME SYSTEMS** MOHammad Khavari Tavana, Mohammad Salehi and Alireza Ejlali  **PCM-FIL: A WRITE-ACTIVITY-AWARE NAND FLASH MEMORY MANAGEMENT SCHEME FOR PCM-BASED EMBEDDED SYSTEM** Duo Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao		FUNCTIONS			
ON THE FEASIBILITY OF LINEAR DISCRETE-TIME SYSTEMS OF THE GREEN SCHEDULING PROBLEM Zheng Li, Pei-Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behl, George Pappas and Rahul Mangharam  TIME-PREDICTABLE COMPUTER ARCHITECTURE FOR CYBER-PHYSICAL SYSTEMS: DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  **Ruppelsaal**  PHOTONET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS Md Yusuf Uddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  TELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  16:00-16:30  Break in Foyer Kuppelsaal  TIMING ANALYSIS OF A PROTECTED OPERATING SYSTEM KERNEL Bernard Blackham, Yao Shi, Sudipta Chattopadhyay, Abhik Roychoudhury and Gernot Heiser  **FEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR HARD REAL-TIME SYSTEMS** Mohammad Khavari Tavana, Mohammad Salehi and Alireza Ejlali  **POM-FTL: A WRITE-ACTIVITY-AWARE NAND FLASH MEMORY MANAGEMENT SCHEME FOR PCM-BASED EMBEDDED SYSTEM Duo Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao	Chair: Enrico Bini	REQUIREMENTS			
SCHEDULING PROBLEM Zheng Li, Pei-Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behl, George Pappas and Rahul Mangharam  TIME-PREDICTABLE COMPUTER ARCHITECTURE FOR CYBER-PHYSICAL SYSTEMS: DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  PHOTONET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS MY YUSU füddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  TELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  16:00-16:30  Break in Foyer Kuppelsaal  TIMING AWALYSIS OF A PROTECTED OPERATING SYSTEM KERNEL Bernard Blackham, Yao Shi, Sudipta Chattopadhyay, Abhik Roychoudhury and Gernot Heiser  FEEDBACK-BASED ENERGY MANAGEMENT IN A STANDBY-SPARING SCHEME FOR HARD REAL-TIME SYSTEMS Mohammad Khavari Tavana, Mohammad Salehi and Alireza Ejlali  PCM-FTL: A WRITE-ACTIVITY-AWARE NAND FLASH MEMORY MANAGEMENT SCHEME FOR PCM-BASED EMBEDDED SYSTEM Duo Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao	13:00-14:00	Lunch in <i>Prechtlsaal</i>			
Session 8: CPS & WSN Applications Kuppelsaal  Chair: Karl-Erik Arzen  DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas Devadas  PHOTONET: A SIMILARITY-AWARE PICTURE DELIVERY SERVICE FOR SITUATION AWARENESS Md Yusuf Uddin, Hongyan Wang, Fatemeh Saremi, Guo-Jun Qi, Tarek Abdelzaher and Thomas Huang  TELOSCAM: IDENTIFYING BURGLAR THROUGH NETWORKED SENSOR-CAMERA MATES WITH PRIVACY PROTECTION Shaojie Tang, Haitao Zhang, Xiang-Yang Li and Jiankang Han  16:00-16:30  Break in Foyer Kuppelsaal  TIMING ANALYSIS OF A PROTECTED OPERATING SYSTEM KERNEL Bernard Blackham, Yao Shi, Sudipta Chattopadhyay, Abhik Roychoudhury and Gernot Heiser  Session 9: Design & Verification II Kuppelsaal  Chair: Tullio Vardanega  DIGITAL EMULATION OF POWER ELECTRONICS SYSTEMS Michel Kinsy, Dusan Majstorovic, Nikola Celanovic, Ivan Celanovic and Srinivas  PHOTONIC: A SIMILARITY-AWARE NAND FLASH MEMORY MANAGEMENT SCHEME FOR PCM-BASED EMBEDDED SYSTEM Duo Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao		SCHEDULING PROBLEM Zheng Li, Pei-Chi Huang, Aloysius Mok, Truong Nghiem, Madhur Behl, George Pappas			
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Chair: Tullio Vardanega  SCHEME FOR PCM-BASED EMBEDDED SYSTEM  Duo Liu, Tianzheng Wang, Yi Wang, Zhiwei Qin and Zili Shao		HARD REAL-TIME SYSTEMS			
18:00-18:30 Closing Toast – Foyer Kuppelsaal	Chair: Tullio Vardanega	SCHEME FOR PCM-BASED EMBEDDED SYSTEM			
	18:00-18:30	Closing Toast – Foyer Kuppelsaal			