Embedded Systems Design and Implementation

1 Speed versus Power

- Metal Oxide Silicon (MOS) Transistors
  - N-Type
  - P-Type

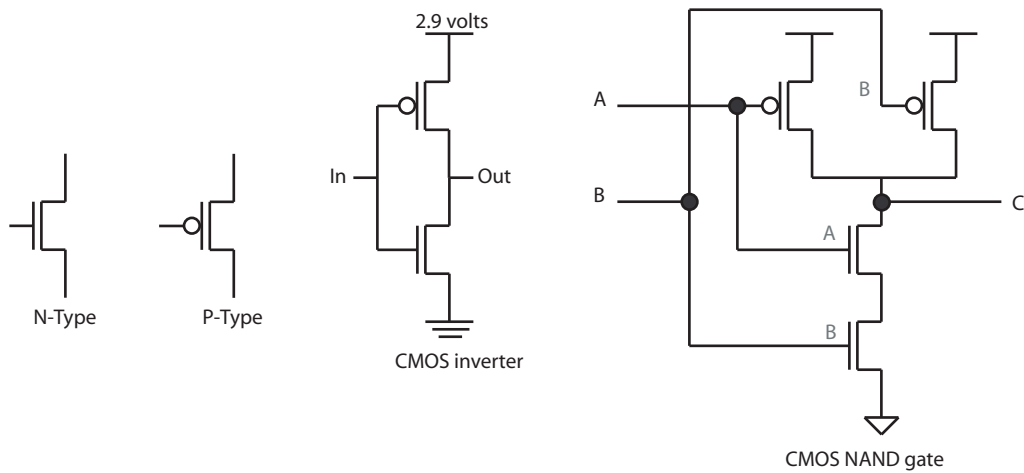- Complementary Metal Oxide Silicon (CMOS)

Figure 1: An illustration of P-type, N-type, and CMOS logics.

  - Static power
  - Dynamic power

2 Design Life-Cycle

- Target system, host computer, cross development

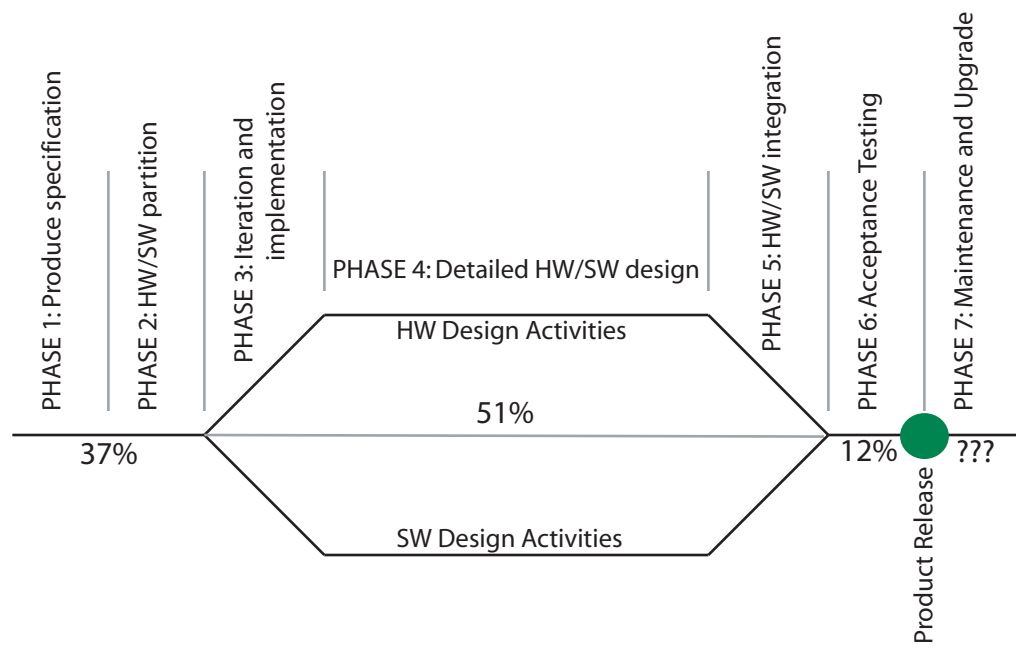- Evaluation board

- HW/SW partitioning
  - Winmodem
  - DMA
  - Memory allocator

Figure 2: An illustration of embedded design life cycle.