

Machine-Level Programming III: Procedures

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Giving credit where credit is due

- Most of slides for this lecture are based on slides created by Drs. Bryant and O'Hallaron, Carnegie Mellon University.
- I have modified them and added new slides.

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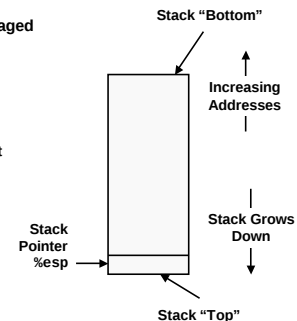
Topics

- IA32 stack discipline
- Register saving conventions
- Creating pointers to local variables

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IA32 Stack

- Region of memory managed with stack discipline
- Grows toward lower addresses
- Register `%esp` indicates lowest stack address
 - address of top element

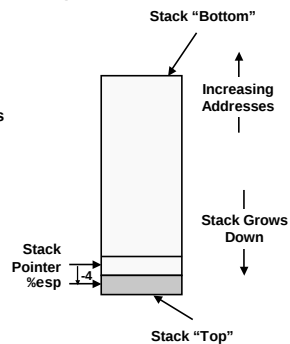


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IA32 Stack Pushing

Pushing

- `pushl Src`
- Fetch operand at `Src`
- Decrement `%esp` by 4
- Write operand at address given by `%esp`

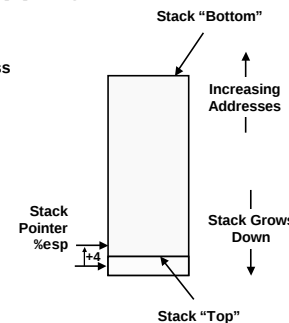


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IA32 Stack Popping

Popping

- `popl Dest`
- Read operand at address given by `%esp`
- Increment `%esp` by 4
- Write to `Dest`



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Stack Operation Examples

		pushl %eax		popl %edx	
0x110		0x110		0x110	
0x10c		0x10c		0x10c	
0x108	123	0x108	123	0x108	123
		0x104	213	0x104	213
%eax	213	%eax	213	%eax	213
%edx	555	%edx	555	%edx	213
%esp	0x108	%esp	0x104	%esp	0x108

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Procedure Control Flow

- Use stack to support procedure call and return

Procedure call:

call *label* Push return address on stack; Jump to *label*

Return address value

- Address of instruction beyond call

- Example from disassembly

```
804854e: e8 3d 06 00 00  call 8048b90 <main>
8048553: 50                pushl %eax
```

- Return address = 0x8048553

Procedure return:

- ret Pop address from stack; Jump to address

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Procedure Call Example

```
804854e: e8 3d 06 00 00  call 8048b90 <main>
8048553: 50                pushl %eax
```

		call 8048b90	
0x110		0x110	
0x10c		0x10c	
0x108	123	0x108	123
		0x104	0x8048553
%esp	0x108	%esp	0x104
%eip	0x804854e	%eip	0x8048b90

%eip is program counter

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Procedure Return Example

```
8048591: c3                ret
```

		ret	
0x110		0x110	
0x10c		0x10c	
0x108	123	0x108	123
0x104	0x8048553	0x104	0x8048553
%esp	0x104	%esp	0x108
%eip	0x8048591	%eip	0x8048553

%eip is program counter

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Stack-Based Languages

Languages that Support Recursion

- e.g., C, Pascal, Java
- Code must be "Reentrant"
 - Multiple simultaneous instantiations of single procedure
- Need some place to store state of each instantiation
 - Arguments
 - Local variables
 - Return pointer

Stack Discipline

- State for given procedure needed for limited time
 - From when called to when return
- Callee returns before caller does

Stack Allocated in *Frames*

- state for single procedure instantiation

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Call Chain Example

Code Structure

```
yoo(...)
{
    .
    .
    who();
    .
}
```

```
who(...)
{
    .
    .
    amI();
    .
    amI();
    .
}
```

```
amI(...)
{
    .
    .
    amI();
    .
}
```

- Procedure amI recursive

Call Chain



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Stack Frames

Contents

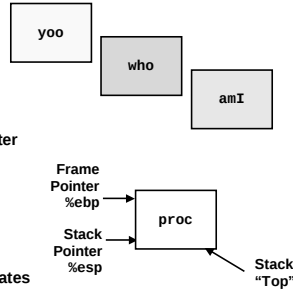
- Local variables
- Return information
- Temporary space

Management

- Space allocated when enter procedure
 - "Set-up" code
- Deallocated when return
 - "Finish" code

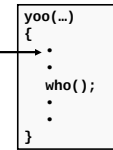
Pointers

- Stack pointer %esp indicates stack top
- Frame pointer %ebp indicates start of current frame



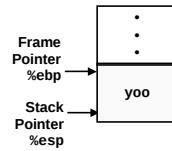
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Stack Operation



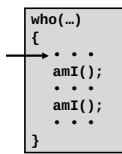
Call Chain

yoo



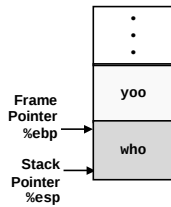
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Stack Operation



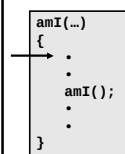
Call Chain

yoo
↓
who



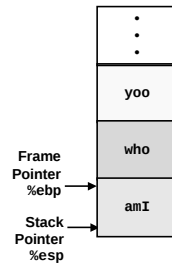
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Stack Operation



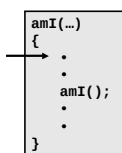
Call Chain

yoo
↓
who
↓
amI



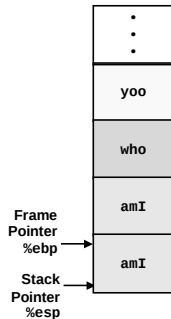
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Stack Operation



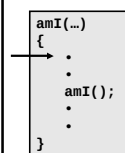
Call Chain

yoo
↓
who
↓
amI
↓
amI



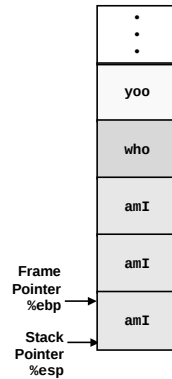
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Stack Operation

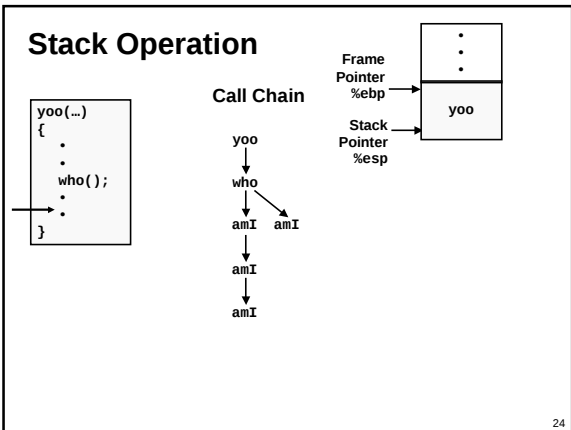
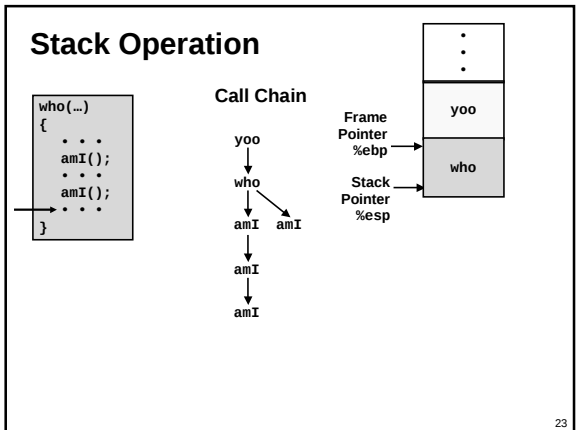
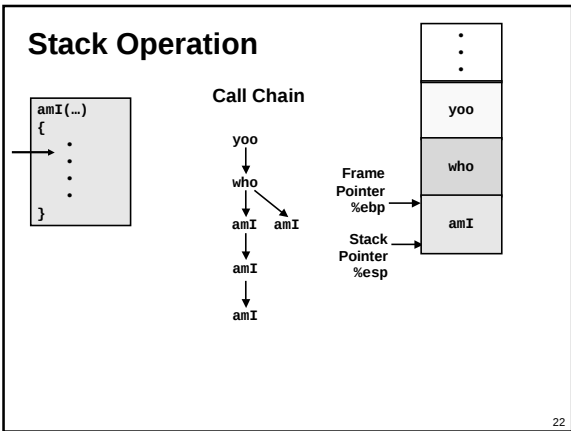
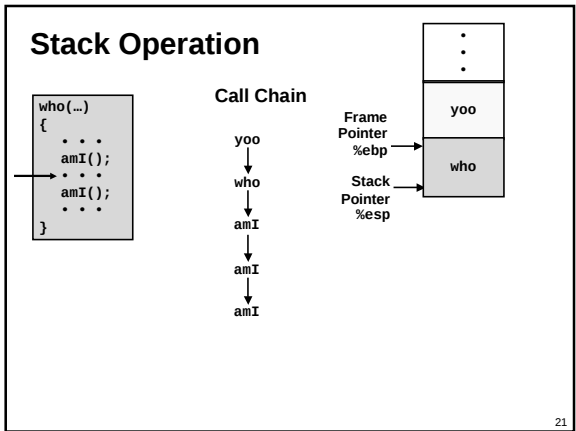
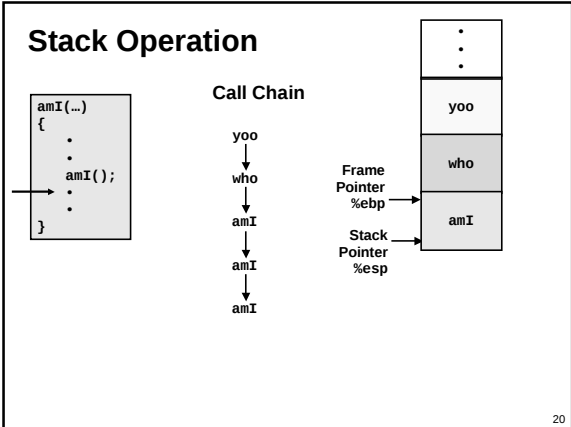
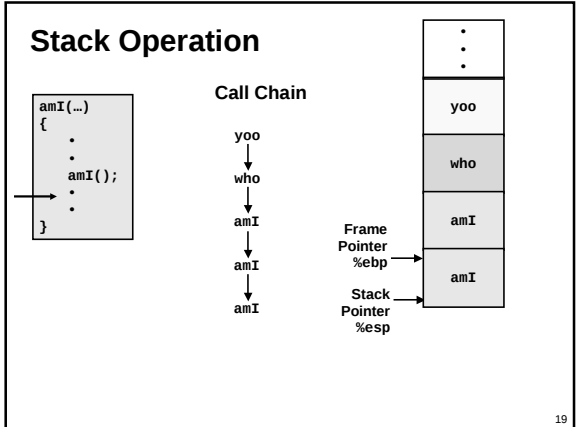


Call Chain

yoo
↓
who
↓
amI
↓
amI
↓
amI



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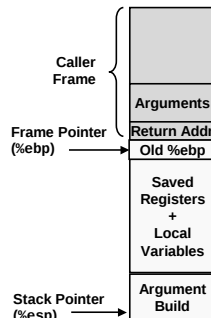
IA32/Linux Stack Frame

Current Stack Frame ("Top" to Bottom)

- Parameters for function about to call
 - "Argument build"
- Local variables
 - If can't keep in registers
- Saved register context
- Old frame pointer

Caller Stack Frame

- Return address
 - Pushed by call instruction
- Arguments for this call



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Revisiting swap

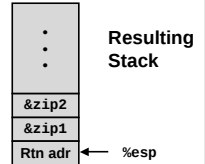
```
int zip1 = 15213;
int zip2 = 91125;
```

```
void call_swap()
{
    swap(&zip1, &zip2);
}
```

```
void swap(int *xp, int *yp)
{
    int t0 = *xp;
    int t1 = *yp;
    *xp = t1;
    *yp = t0;
}
```

Calling swap from call_swap

```
call_swap:
    . . .
    pushl $zip2 # Global Var
    pushl $zip1 # Global Var
    call swap
    . . .
```



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Revisiting swap

```
void swap(int *xp, int *yp)
{
    int t0 = *xp;
    int t1 = *yp;
    *xp = t1;
    *yp = t0;
}
```

```
swap:
    pushl %ebp          } Set Up
    movl %esp,%ebp
    pushl %ebx

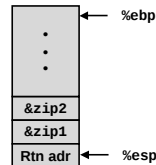
    movl 12(%ebp),%ecx  } Body
    movl 8(%ebp),%edx
    movl (%ecx),%eax
    movl (%edx),%ebx
    movl %eax,(%edx)
    movl %ebx,(%ecx)

    movl -4(%ebp),%ebx  } Finish
    movl %ebp,%esp
    popl %ebp
    ret
```

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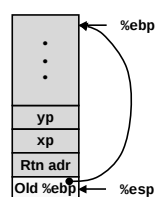
swap Setup #1

Entering Stack



```
swap:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
```

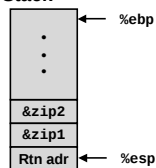
Resulting Stack



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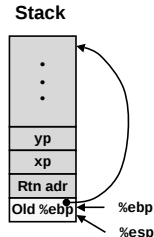
swap Setup #2

Entering Stack



```
swap:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
```

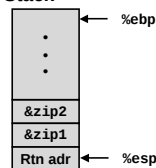
Resulting Stack



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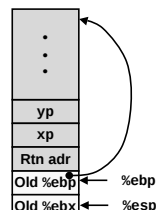
swap Setup #3

Entering Stack



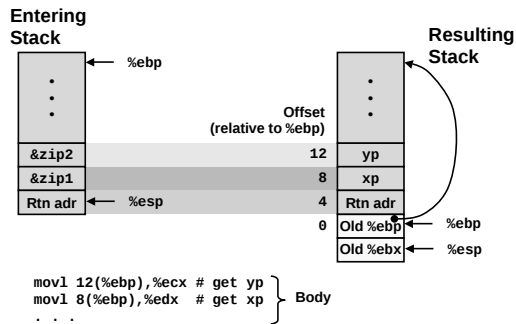
```
swap:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
```

Resulting Stack



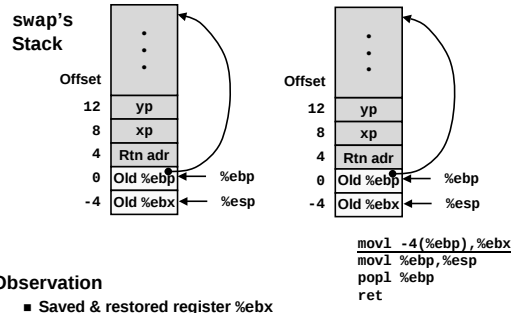
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Effect of swap Setup



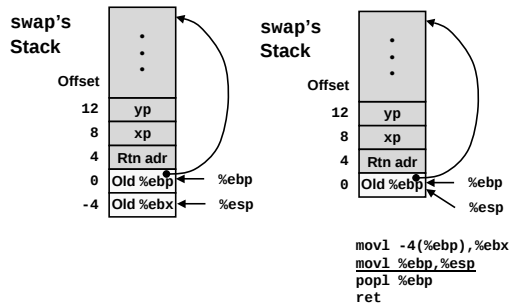
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swap Finish #1



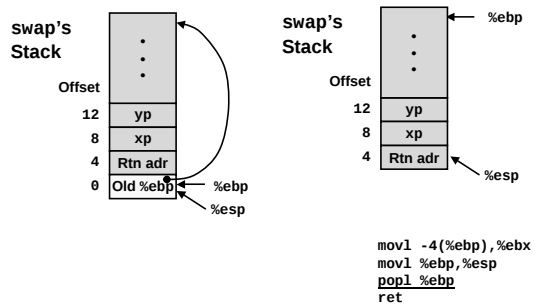
32

swap Finish #2



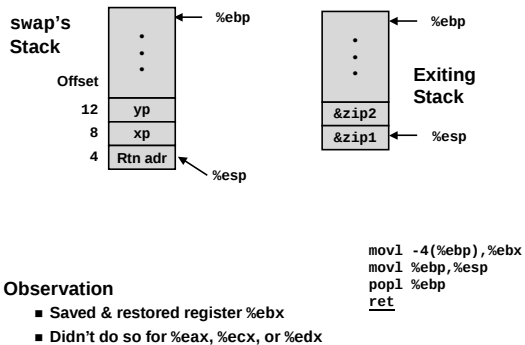
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swap Finish #3



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swap Finish #4



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Register Saving Conventions

When procedure yoo calls who:

- yoo is the *caller*, who is the *callee*

Can Register be Used for Temporary Storage?

```
yoo:
    ...
    movl $15213, %edx
    call who
    addl %edx, %eax
    ...
    ret
```

```
who:
    ...
    movl 8(%ebp), %edx
    addl $91125, %edx
    ...
    ret
```

- Contents of register %edx overwritten by who

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Register Saving Conventions

When procedure yoo calls who:

- yoo is the *caller*, who is the *callee*

Can Register be Used for Temporary Storage?

Conventions

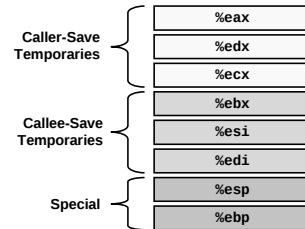
- "Caller Save"
 - Caller saves temporary in its frame before calling
- "Callee Save"
 - Callee saves temporary in its frame before using

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IA32/Linux Register Usage

Integer Registers

- Two have special uses
 - %ebp, %esp
- Three managed as callee-save
 - %ebx, %esi, %edi
 - Old values saved on stack prior to using
- Three managed as caller-save
 - %eax, %edx, %ecx
 - Do what you please, but expect any callee to do so, as well
- Register %eax also stores returned value



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Recursive Factorial

```
int rfact(int x)
{
    int rval;
    if (x <= 1)
        return 1;
    rval = rfact(x-1);
    return rval * x;
}
```

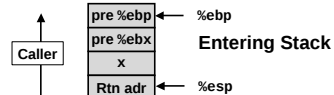
Registers

- %eax used without first saving
- %ebx used, but save at beginning & restore at end

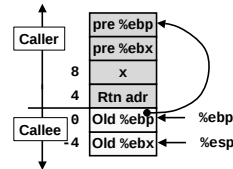
```
.globl rfact
.type rfact,@function
rfact:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
    movl 8(%ebp),%ebx
    cmpl $1,%ebx
    jle .L78
    leal -1(%ebx),%eax
    pushl %eax
    call rfact
    imull %ebx,%eax
    jmp .L79
    .align 4
.L78:
    movl $1,%eax
.L79:
    movl -4(%ebp),%ebx
    movl %ebp,%esp
    popl %ebp
    ret
```

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Rfact Stack Setup



```
rfact:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
```



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Rfact Body

Recursion

```
movl 8(%ebp),%ebx # ebx = x
cmpl $1,%ebx # Compare x : 1
jle .L78 # If <= goto Term
leal -1(%ebx),%eax # eax = x-1
pushl %eax # Push x-1
call rfact # rfact(x-1)
imull %ebx,%eax # rval * x
jmp .L79 # Goto done
.L78: # Term:
movl $1,%eax # return val = 1
.L79: # Done:
```

```
int rfact(int x)
{
    int rval;
    if (x <= 1)
        return 1;
    rval = rfact(x-1);
    return rval * x;
}
```

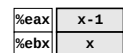
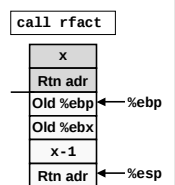
Registers

- %ebx Stored value of x
- %eax
 - Temporary value of x-1
 - Returned value from rfact(x-1)
 - Returned value from this call

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Rfact Recursion

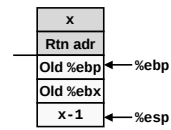
```
leal -1(%ebx),%eax
```



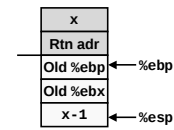
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Rfact Result

Return from Call



imull %ebx,%eax



%eax	(x-1)!
%ebx	x

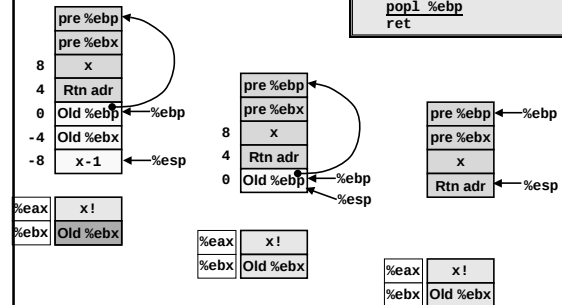
Assume that rfact(x-1) returns (x-1)! in register %eax

%eax	x!
%ebx	x

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Rfact Completion

```
movl -4(%ebp),%ebx
movl %ebp,%esp
popl %ebp
ret
```



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Pointer Code

Recursive Procedure

```
void s_helper
(int x, int *accum)
{
    if (x <= 1)
        return;
    else {
        int z = *accum * x;
        *accum = z;
        s_helper (x-1, accum);
    }
}
```

Top-Level Call

```
int sfact(int x)
{
    int val = 1;
    s_helper(x, &val);
    return val;
}
```

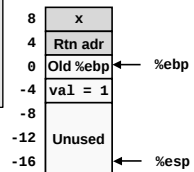
- Pass pointer to update location

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Creating & Initializing Pointer

Initial part of sfact

```
_sfact:
    pushl %ebp          # Save %ebp
    movl %esp,%ebp      # Set %ebp
    subl $16,%esp       # Add 16 bytes
    movl 8(%ebp),%edx    # edx = x
    movl $1,-4(%ebp)    # val = 1
```



Using Stack for Local Variable

- Variable val must be stored on stack
 - Need to create pointer to it
- Compute pointer as -4(%ebp)
- Push on stack as second argument

```
int sfact(int x)
{
    int val = 1;
    s_helper(x, &val);
    return val;
}
```

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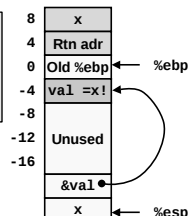
Passing Pointer

Calling s_helper from sfact

```
leal -4(%ebp),%eax # Compute &val
pushl %eax         # Push on stack
pushl %edx         # Push x
call s_helper      # call
movl -4(%ebp),%eax # Return val
...               # Finish
```

```
int sfact(int x)
{
    int val = 1;
    s_helper(x, &val);
    return val;
}
```

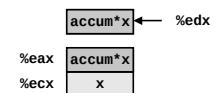
Stack at time of call



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Using Pointer

```
void s_helper
(int x, int *accum)
{
    ...
    int z = *accum * x;
    *accum = z;
    ...
}
```



```
...
movl %ecx,%eax # z = x
imull (%edx),%eax # z *= *accum
movl %eax,(%edx) # *accum = z
...
```

- Register %ecx holds x
- Register %edx holds pointer to accum
 - Use access (%edx) to reference memory

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Summary

The Stack Makes Recursion Work

- Private storage for each *instance* of procedure call
 - Instantiations don't clobber each other
 - Addressing of locals + arguments can be relative to stack positions
- Can be managed by stack discipline
 - Procedures return in inverse order of calls

IA32 Procedures Combination of Instructions + Conventions

- Call / Ret instructions
- Register usage conventions
 - Caller / Callee save
 - %ebp and %esp
- Stack frame organization conventions

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